Missile Defence System - ADLAS

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# Abstract

The Main aspect of this paper is to create, develop and test a missile safety check system that will run through tests on the various parts of the projectile. Systems of this nature are often complex and perform high levels of algorithms to calculate correct values for ensuring safety. Moreover, designing a system of this nature will allow a certain level of understanding on how to map a system to requirements and ensure the re-usability.

The end goal of this project will enhance the understanding of how the system will cope with unexpected scenario’s and will affect the mission outcome, once the system has been tested. All quality and event variables will have collected to be up and hence analysed to find a measure of performance that is useful in assessing the validity of the system tested and also to seek a final evaluation of what is happening.

**Keywords = Ada, Requirements, Traceability, V software lifecycle**

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# List of Abbreviations

|  |  |
| --- | --- |
| Abbreviation | Description |
| AP | Autopilot |
| BS | Base Station |
| FPT | Flight Phase Tests |
| HLD | High Level Design |
| LLD | Low Level Design |
| LPT | Launch Phase Tests |
| IPT | Initialise Phase Tests |
| MRT | Mode Run Through |
| SDLC | Software Development Life Cycle |
| SPT | Simulation Phase Tests |
|  |  |

# Change History

|  |  |  |  |
| --- | --- | --- | --- |
| Version | Date | Comment | Author |
| 1.0 | 20/12/2017 | Sections complete to baseline – ready for first release | SC |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |

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# 1 Introduction

## 1.1 Background

For this project, it was the idea to focus on an idea that has become very important in today's defense society. It has become apparent that missiles can be fired from any point of the earth and with the speed capabilities and high probability of not being able to destroy the object before it hits its intended target. I wanted to focus on something I found fascinating and to really look at how complex of a system of this nature would be and if it was capable of being miniaturized onto a STM32F4 board and coded in Ada 95 which is a strongly typed language.

This system needs to calculate what sensor readings and diagnostics are acceptable and whether it would be possible within the simulation set calculations that all systems are performing reliably. Throughout this project, I will look at various software techniques used to work out the most efficient way of software development and if it’s possible to ensure that software quality can be maintained throughout the project.

## 

## 1.2 Aim

The overall focus of this project is to set requirements, design, implement and code a complex missile defense system.

## 1.3 Objectives

* Review theoretical aspects of software development
* Review components of missiles and counter measures
* Review ADA software
* Set requirements for the system
* Design and code the system
* Provide verification of software meeting requirements
* Perform system and unit testing
* Produce conclusions on the project

## 1.4 Design Points of Objectives

This section is to explain each objective and how they will be reached, it will then be reviewed in the last chapter Conclusion to see if it was completed.

* Review theoretical aspects of software development – The project will be looking at SDLC models and which model will fit the project suitably and what the different advantages and disadvantages are.
* Review components of missiles and counter measures – Further review into physical aspects of missiles and how they operate, what measures can be used to counter and how current systems calculate choices.
* Review ADA software – Researching how Ada is used to be safety and mission critical and aid the user to falling into similar coding pitfalls.
* Set requirements for the system –Create requirements for what the system should operate to and ensure that traceability of requirements to code is followed throughout.
* Design and code the system – Create the system required for main functionality to match the specification
* Provide verification of software meeting requirements – Ensuring that the code is fulfilling what it was designed to do.
* Perform system and unit testing – Ensuring the values and system performs the expected results
* Produce Conclusions for the project – Explain the positives and negatives for the project and whether the end product matched the design set out.

## 1.5 System Overview

A circuit board

Description generated with high confidence

## 

The ADLAS and other related platform equipment function together to:

* Control and monitor the release and system critical information
* Main Scheduler calls other phase schedulers
* Runs through phase tests before launch
* Preparing ADLAS for Release
* Post launch phase tests conducted

## 1.6 Dependencies

I will provide an AdaCore Target Build environment. This will enable us to compile their code against the target minimal Ada run-time we are using.

An AdaCore Native Build environment will enable this system to be compiled and allow the code to be used for testing purposes such as Unit Testing.

In order to continue to maintain the signal to register bit mapping in a single place, it is expected that the Release DevBase packages should use the Register package to determine the correct register and bits associated with each signal.

The ADLAS Packages listed below:

* IPT
* SPT
* LPT
* FPT
* General\_Checks
* Simulation
* Monitor
* Failure
* Register
* Types

The different phases of ADLAS tests will be scheduled by the Schedule package. This Scheduler package will decide which Phase routines to call as well as when to call them based on the information in the ADLAS Definition Document. The ADLAS Test package will retrieve a copy of all the appropriate registers from the Inputs at the start of the frame to ensure a consistent set of input values for all the tests.

Each ADLAS Test will perform its required check, checking the appropriate register bits, and reporting any failure by calling the failure\_log-log\_failure routine with a Fault Data Record. The content of the fields of the Fault Data Record is defined in the ADLAS definition document.

## 1.8 IPT

This static class controls the execution of the ADLAS checks required to be performed during the First phase.

The IPT package shall provide the following operations:

### 1.8.1 Procedure Initialise;

The Initialise routine shall initialise all internal state of the class.

### 1.8.2 Procedure Schedule\_Phase;

The Schedule\_ Phase routine shall perform all the tests defined as IPT Tests in the ADLAS Definition Document. Once the complete set of tests has been performed, the sequence allows passage to the next phase or stops the function.

## 1.9 SPT

This static class controls the execution of the ADLAS checks required to be performed during the Second phase.

The SPT package shall provide the following operations:

### 1.9.1 Procedure Initialise;

The Initialise routine shall initialise all internal state of the class.

### 1.9.2 Procedure Schedule\_Phase;

The Schedule\_ Phase routine shall perform all the tests defined as SPT Tests in the ADLAS Definition Document. Once the complete set of tests has been performed, the sequence allows passage to the next phase or stops the function.

## 1.10 LPT

This static class controls the execution of the ADLAS checks required to be performed during the Third phase.

The LPT package shall provide the following operations:

### 1.10.1 Procedure Initialise;

The Initialise routine shall initialise all internal state of the class.

### 1.10.2 Procedure Schedule\_Phase;

The Schedule\_ Phase routine shall perform all the tests defined as LPT Tests in the ADLAS Definition Document. Once the complete set of tests has been performed, the sequence allows passage to the next phase or stops the function.

## 1.11 FPT

This static class controls the execution of the ADLAS checks required to be performed during the Fourth phase.

The FPT package shall provide the following operations:

### 1.11.1 Procedure Initialise;

The Initialise routine shall initialise all internal state of the class.

### 1.11.2 Procedure Schedule\_Phase;

The Schedule\_Phase routine shall perform all the tests defined as FPT Tests in the ADLAS Definition Document. Once the complete set of tests has been performed, the sequence allows passage to the next phase or stops the function.

## 1.12 General\_Checks

This class contains the ADLAS Checks that are mutual to more than one package to save on repeatability of procedures this package will allow cross use.

## 1.13 Verification Activities

I shall perform the following Verification Activities on the Ada packages I develop and provide a test report for each:

* Package

## 1.12 Package Requirements

## General Requirements

|  | Software shall comprise the following Phase Modes:  Initialised Phase Tests  Simulation Phase Tests  Launch Phase Tests  Flight Phase Tests |
| --- | --- |

## IPT Requirements

|  | IPT shall be performed automatically and without any intervention of the user as soon as the Power-Up Manager Application passes the control to the Control Monitor. |
| --- | --- |
|  | IPT shall test Holding and Locking inputs for their expected default and self-consistent states. |
|  | Whenever IPT detects a failure the relevant fault shall be logged calling the Failure\_Log operations. |
|  | IPT shall complete within 3 minutes from Control passover. |

## SPT Requirements

|  | IPT shall be performed automatically and without any intervention of the user as soon as the IPT pass the minumum required locking safety checks. |
| --- | --- |
|  | The SPT shall perform non-intrusive testing of the expected flight path and relevant issues of the launch release, emergency misfire and emergency jettison hardware interface. |
|  | SPT shall be run under guaranted time frame which is to be agreed upon flight path needs. |
|  | Whenever SPT detects a possible failure the relevant fault shall be logged calling the Failure\_Log operations. |

## LPT Requirements

|  | LPT shall not run unless IPT and SPT pass over control. |
| --- | --- |
|  | LPT shall complete within given timeframe. |
|  | Whenever LPT detects a failure the relevant fault shall be logged calling the Failure\_Log operations. |
|  | LPT will run when access to fire has been given and all other checks are at the given threshold. |

## FPT Requirements

|  | FPT shall not run unless LPT passes the main sequence events. |
| --- | --- |
|  | FPT shall complete within the given SPT timeframe. |
|  | Whenever FPT detects a failure the relevant fault shall be logged calling the Failure\_Log operations. |
|  | FPT will run when LPT passes over control. |

## General\_Checks Requirements

|  | General\_Checks will adopt the requirements of whichever phase it is called from. |
| --- | --- |

## Simulation Requirements

|  | Simulation Requirements shall explore various stages of warhead detonation and if possible risks may occur. |
| --- | --- |

## Monitor Requirements

|  | Monitor will adopt the requirements of whichever phase it is called from. |
| --- | --- |

## Failure Requirements

|  | General\_Checks will adopt the requirements of whichever phase it is called from. |
| --- | --- |

## Register Requirements

|  | Register |
| --- | --- |

# 2.0 System Design Packages

## 2.1 Test ID

This section will be the explanation of the Test Id that will have a one to many relationship with a procedure; it will explain how it is identified and how to calculate which Group (Package) and subgroup (Child Package) the test and procedure belong to. The Id will be an 8-digit enum character prefixed with ID\_ for Ada syntax to allow correct usage; it will be saved as a 32-bit value to allow easy transfer to the system’s data registers.

**ID\_12233444**

**1 = Group**

**2 = Sub-Group**

**3 = Procedure**

**4 = Test**

This value above has 8-digits, the first being the Group for example in Section A will reference that IPT has a group ID offset of 10,000,000 which means any Test\_ID with a start value of 1 will be in this group. For 22 this is the sub-group and for example in Section A will reference that IPT has a sub-group ID offset of 100,000 which means any Test\_ID with a start value of 101 will be in this group 1 and sub-group 1 of IPT group. 33 is the procedure key inside the ID this gives it a range of 100 procedures per sub-group. 444 is the count of tests per procedure and it has a max value of 999 for each procedure.

## 2.2 Section A

|  |  |  |  |
| --- | --- | --- | --- |
| Group | Sub-Group | Group ID Offset | Sub Group ID Offset |
| IPT | Non-tests | 10,000,000 | 100,000 |
| IPT | Primary\_Lock\_Interface | 10,000,000 | 200,000 |
| IPT | Secondary\_Lock\_Interface | 10,000,000 | 300,000 |
| IPT | Warhead | 10,000,000 | 400,000 |
| IPT | Communications | 10,000,000 | 500,000 |
| SPT | Non-tests | 20,000,000 | 100,000 |
| SPT | Radar | 20,000,000 | 200,000 |
| SPT | Sensor Interface | 20,000,000 | 300,000 |
| SPT | Geometrical | 20,000,000 | 400,000 |
| SPT | HP1 Bus Interface | 20,000,000 | 500,000 |
| SPT | Autopilot | 20,000,000 | 600,000 |
| LPT | Non-tests | 30,000,000 | 100,000 |
| LPT | Initiation | 30,000,000 | 200,000 |
| LPT | Watchdog | 30,000,000 | 300,000 |
| LPT | Integrity | 30,000,000 | 400,000 |
| FPT | Non-tests | 40,000,000 | 100,000 |
| FPT | Detonation | 40,000,000 | 200,000 |
| FPT | Boost | 40,000,000 | 300,000 |
| FPT | Warhead\_Release | 40,000,000 | 400,000 |
| General\_Checks | Failure | 50,000,000 | 100,000 |
| General\_Checks | Warhead | 50,000,000 | 200,000 |
| General\_Checks | Navigation | 50,000,000 | 300,000 |
| General\_Checks | Logic Power Supply | 50,000,000 | 400,000 |
| General\_Checks | Fuel Burn | 50,000,000 | 500,000 |
| General\_Checks | Common | 50,000,000 | 600,000 |

## 

## 2.3 Section B

Phase Test Procedures and their algorithms are listed in the Section B&C document.

### 2.3.1 Register Package

|  |  |
| --- | --- |
| Register\_Types | |
| Requirements | |
|  |  |
|  |  |

## 2.4 Section C

Phase Test Descriptions are listed in the Section B&C document.